

IC (Top View)

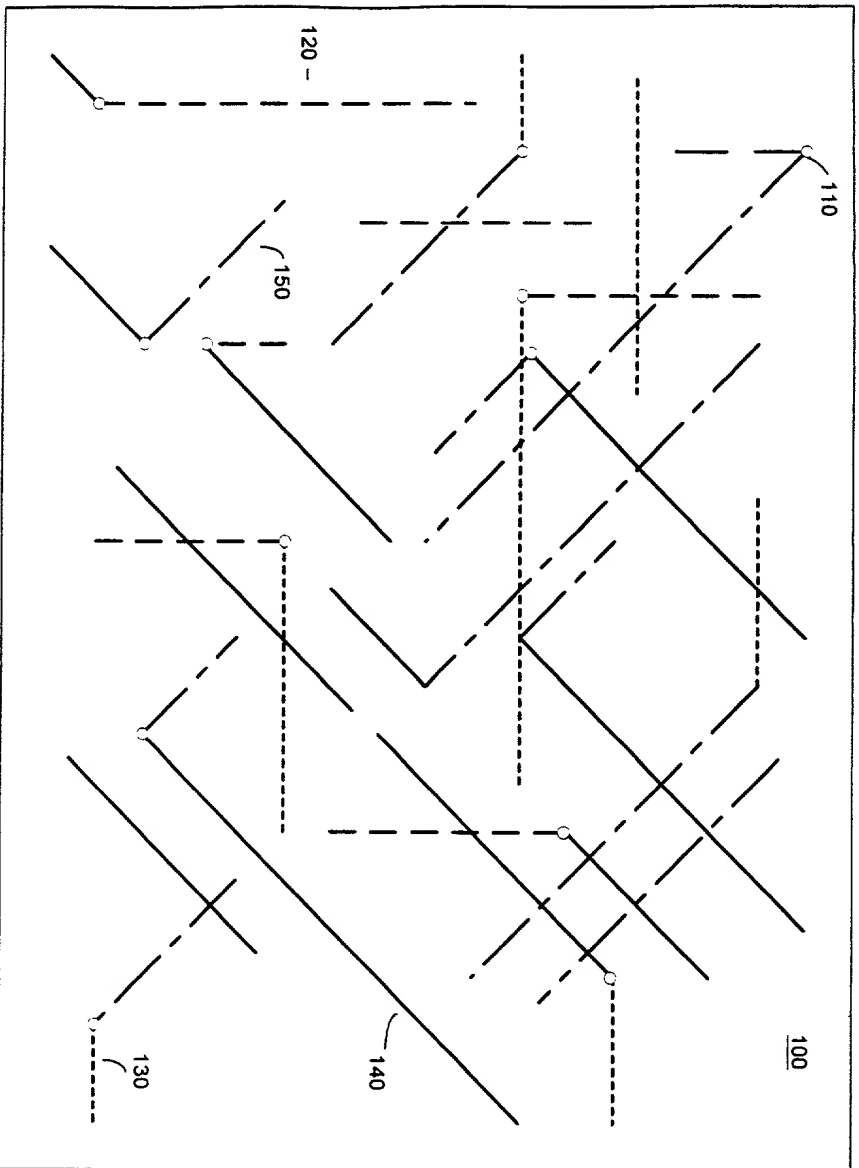
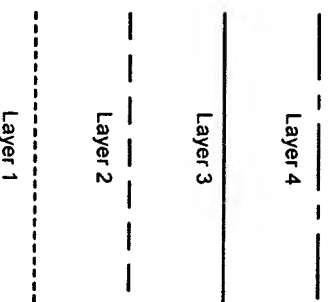


Figure 1a



# IC (Top View)

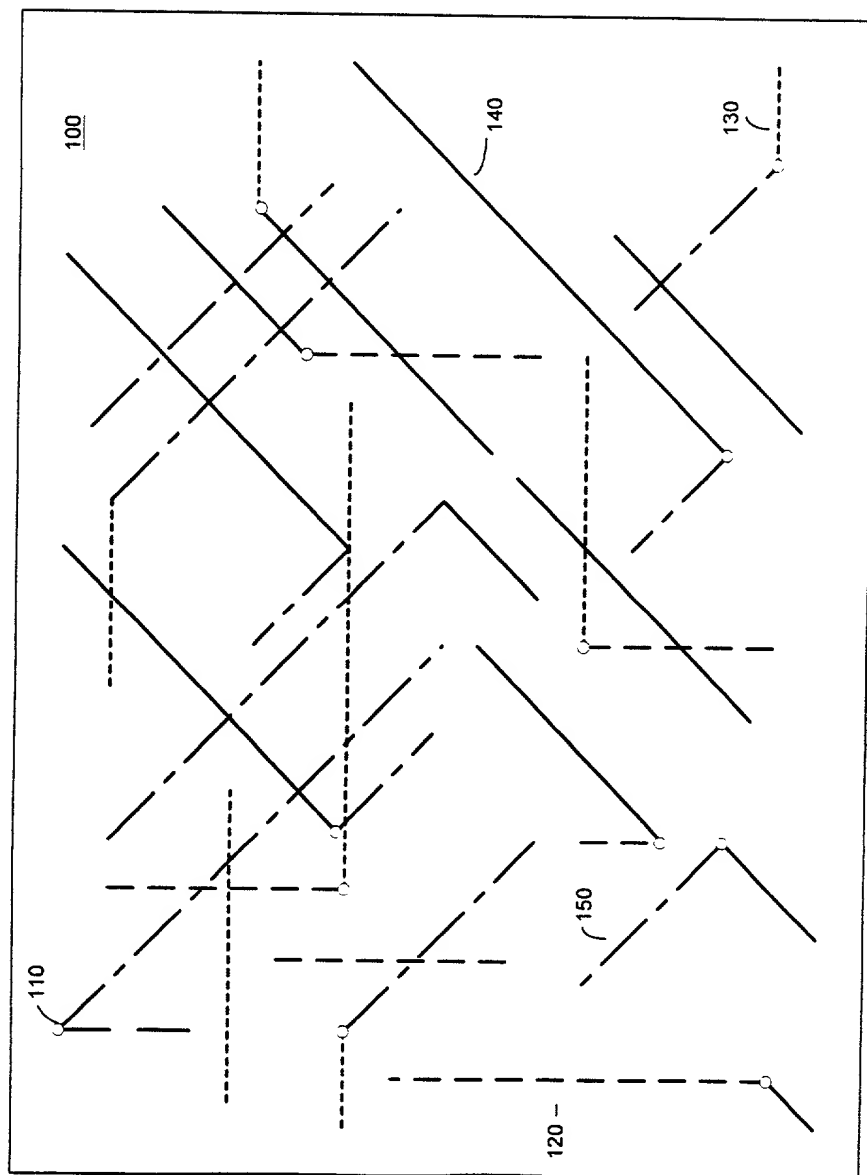


Figure 1a

IC (Top View)

155

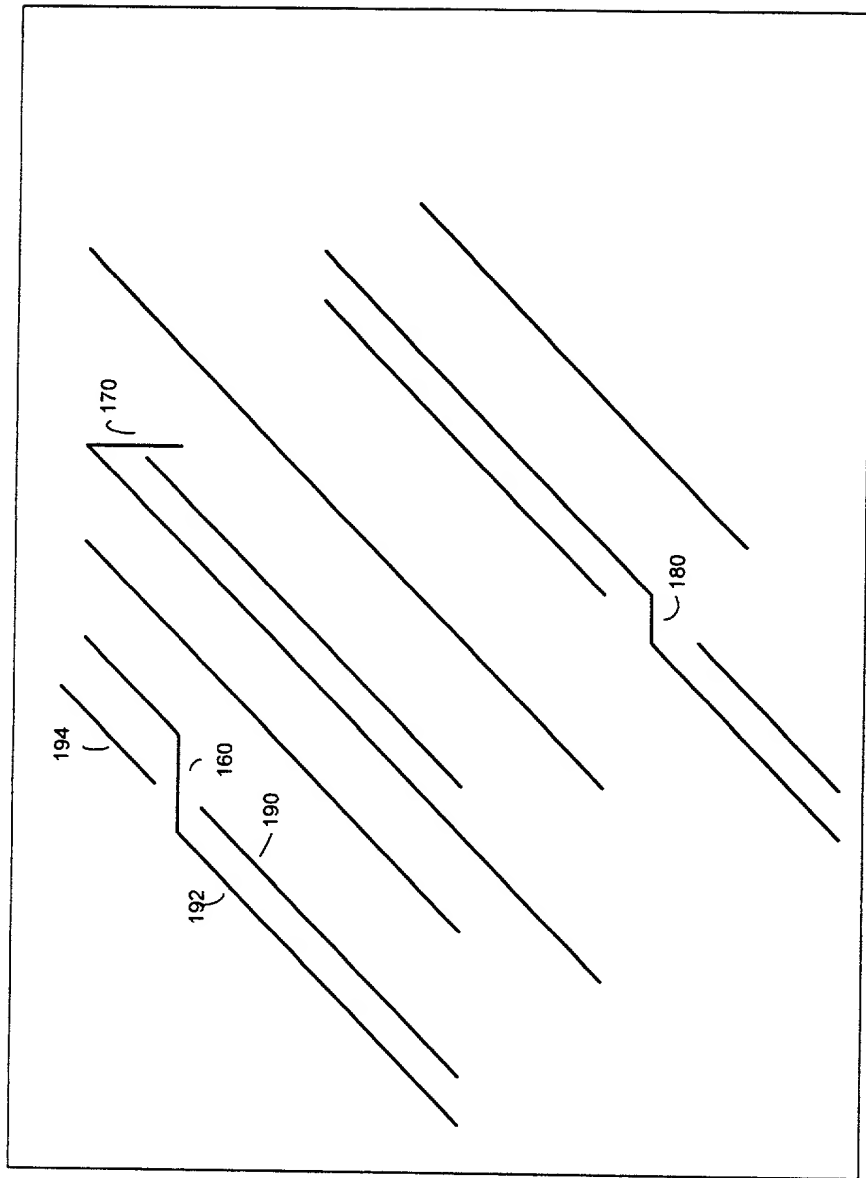
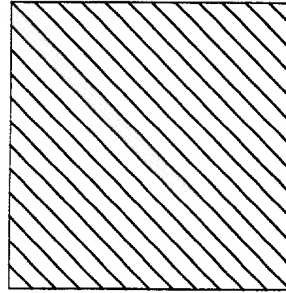
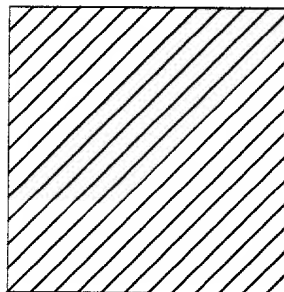


Figure 1b



Layer "n+1"

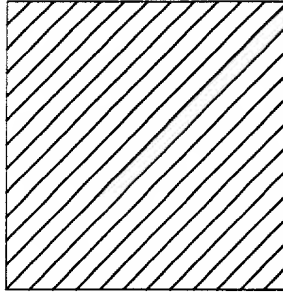
Octalinear (-45 Deg.)



Layer "n"

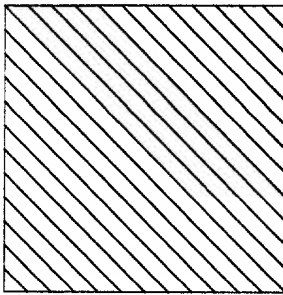
Octalinear (+45 Deg.)

**Figure 2a**



Layer "n+1"

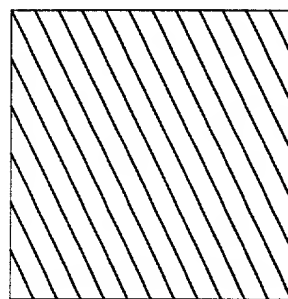
Octalinear (+45 Deg.)



Layer n

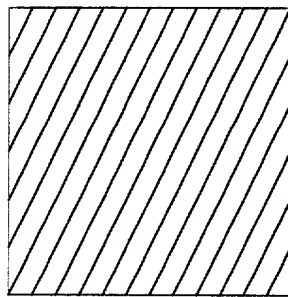
Octalinear (-45 Deg.)

**Figure 2b**



Layer "n+1"

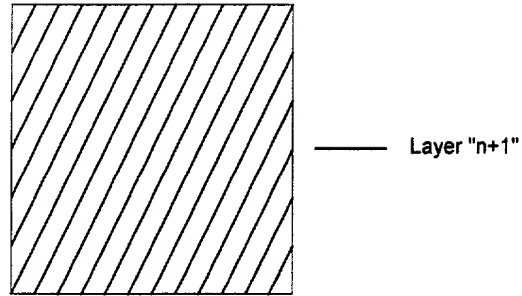
(-60 Degrees)



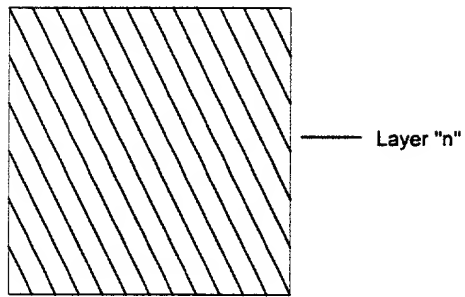
Layer "n"

(+60 Degrees)

**Figure 3a**



(+60 Degrees)



(-60 Degrees)

**Figure 3b**

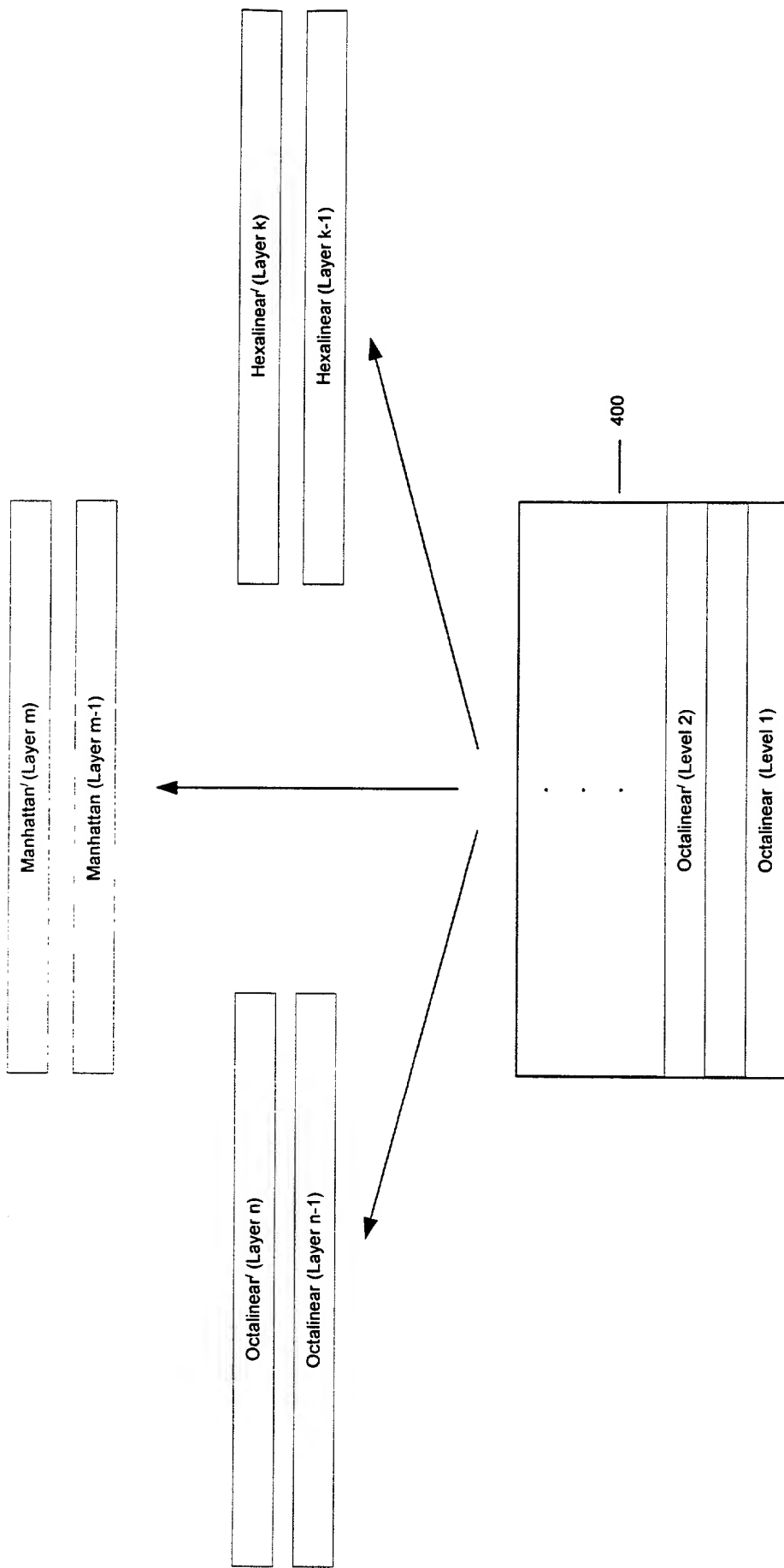


Figure 4a



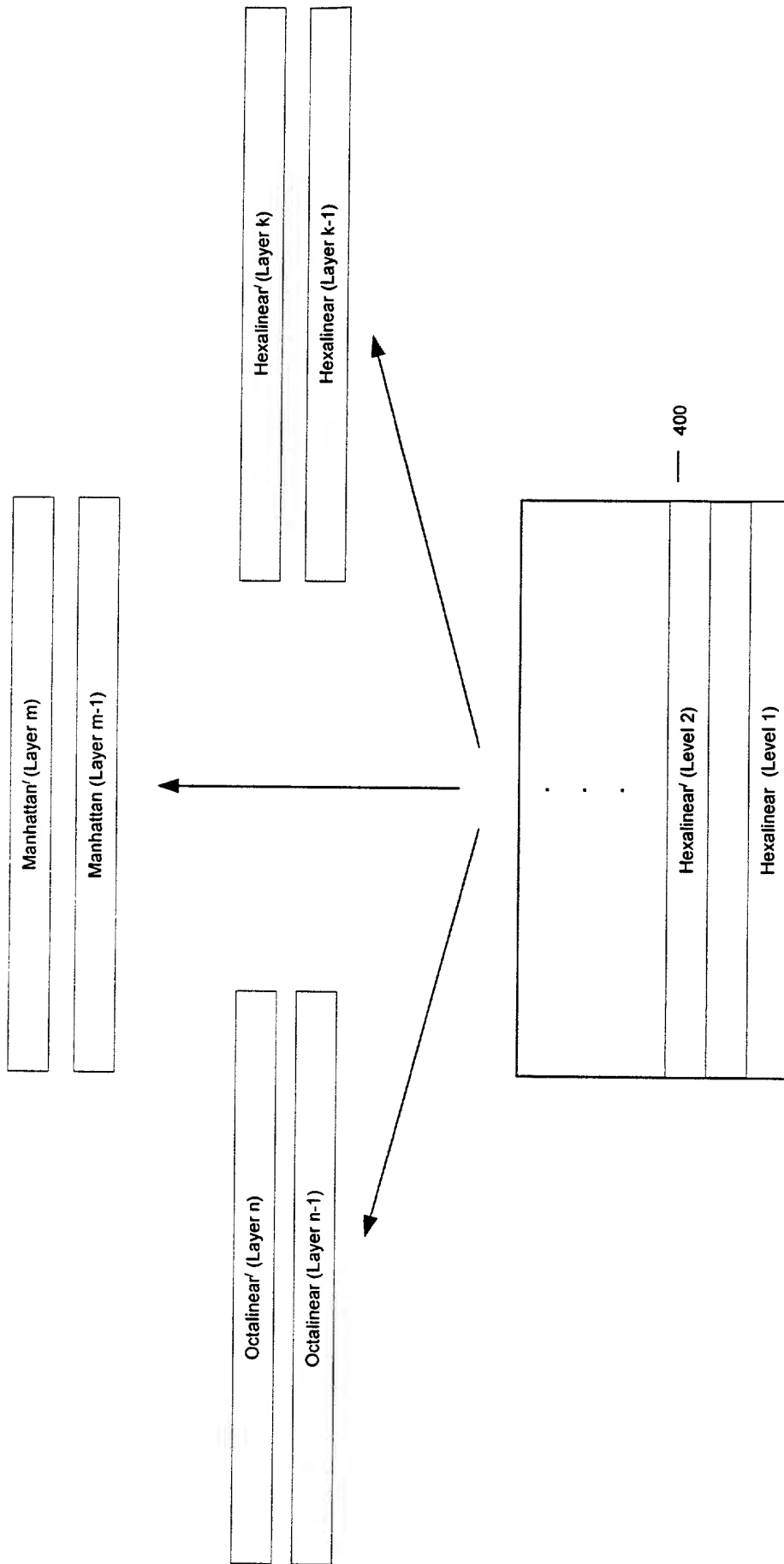


Figure 4b



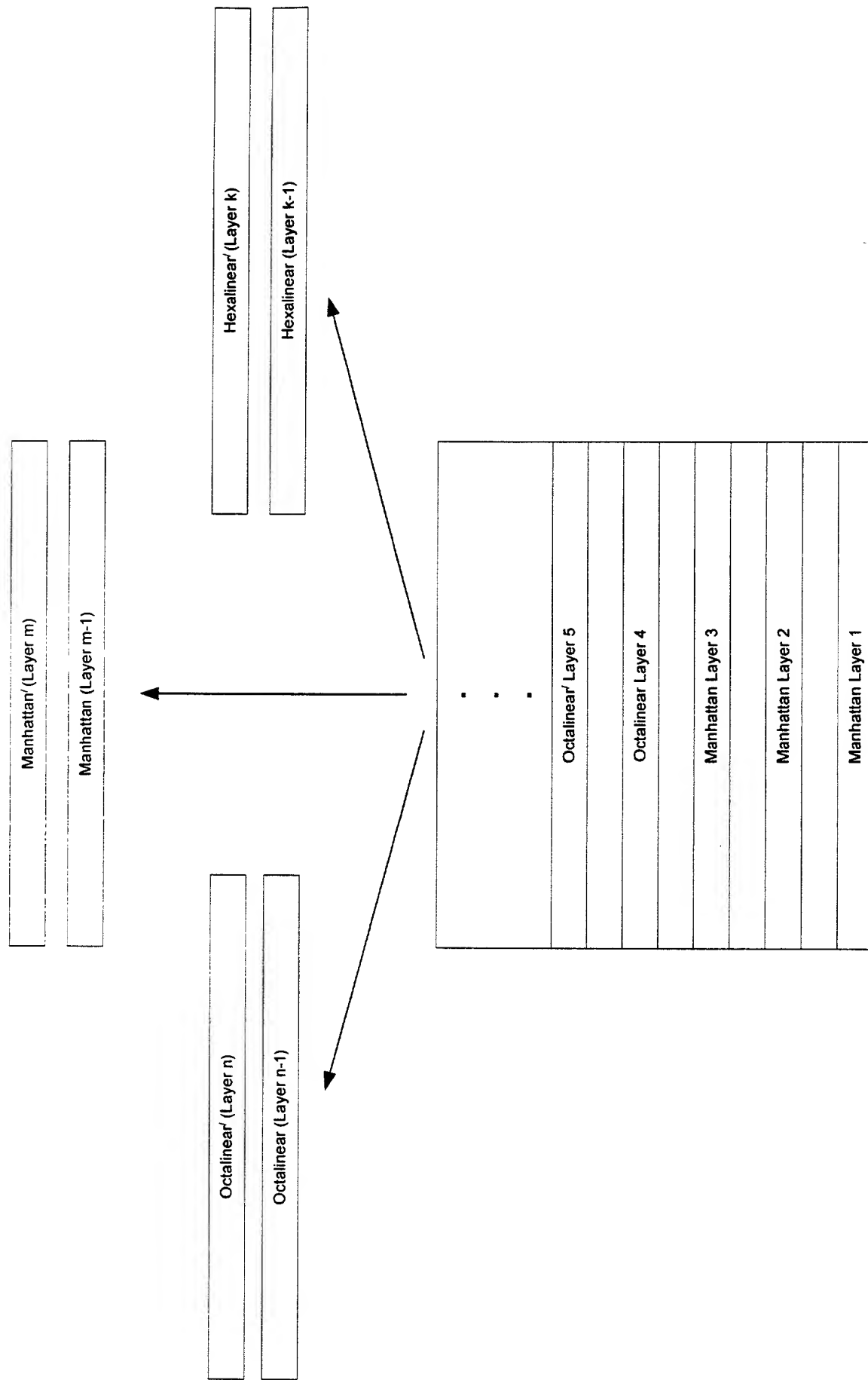
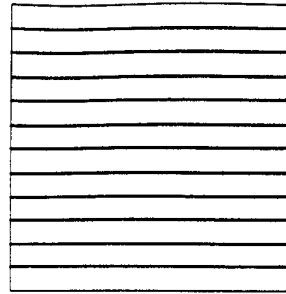
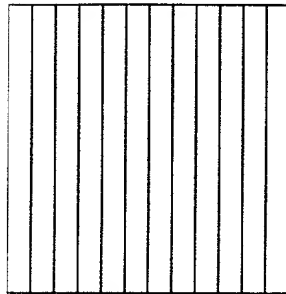


Figure 5b



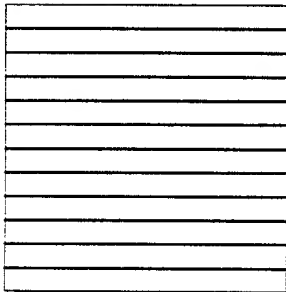
Layer 3

Horizont



Layer 2

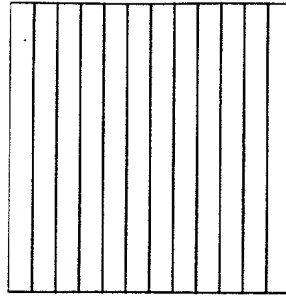
Vertical



Layer 1

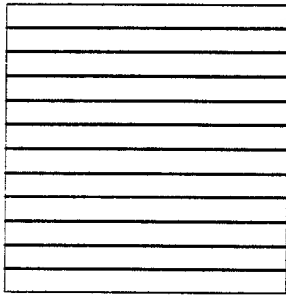
Horizont

**Figure 6a**



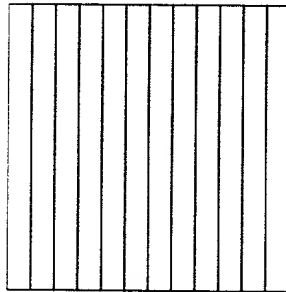
Layer 3

Vertical



Layer 2

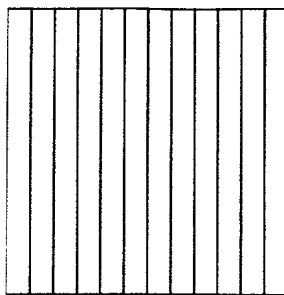
Horizont



Layer 1

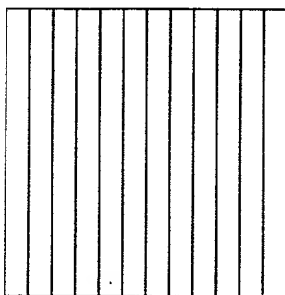
Vertical

**Figure 6b**



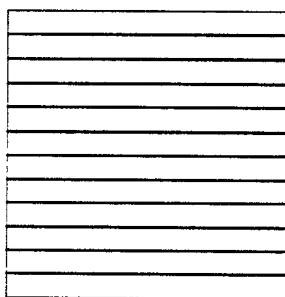
Layer 3

Vertical



Layer 2

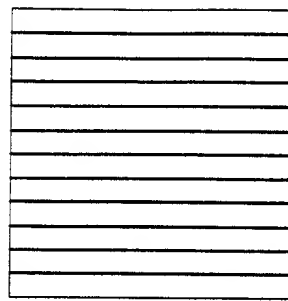
Vertical



Layer 1

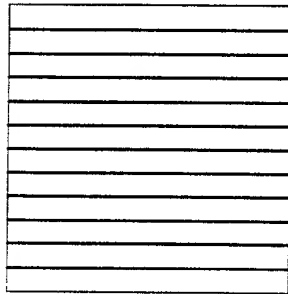
Horizont

**Figure 6c**



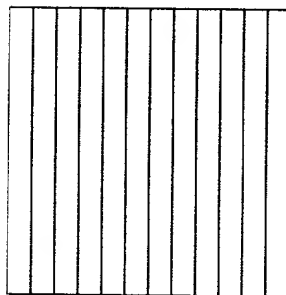
Layer 3

Horizont



Layer 2

Horizont



Layer 1

Vertical

**Figure 6d**

Figure 7: A diagram illustrating a multi-layered architecture. The central component is a large rectangle divided into five horizontal sections, labeled from top to bottom: Hexalinear Layer 5, Hexalinear Layer 4, Manhattan Layer 3, Manhattan Layer 2, and Manhattan Layer 1. Above this central block, two arrows point outwards. The left arrow points to a vertical stack of two rectangles labeled Octalinear (Layer n) and Octalinear (Layer n-1). The right arrow points to a vertical stack of two rectangles labeled Hexalinear (Layer k) and Hexalinear (Layer k-1). To the right of the central block, two more arrows point outwards. The top arrow points to a vertical stack of two rectangles labeled Manhattan (Layer m) and Manhattan (Layer m-1). The bottom arrow points to a vertical stack of two rectangles labeled Hexalinear (Layer k) and Hexalinear (Layer k-1).

Manhattan (Layer m)

Manhattan (Layer m-1)

Octalinear (Layer n)

Octalinear (Layer n-1)

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Hexalinear Layer 5
Hexalinear Layer 4
Manhattan Layer 3
Manhattan Layer 2
Manhattan Layer 1

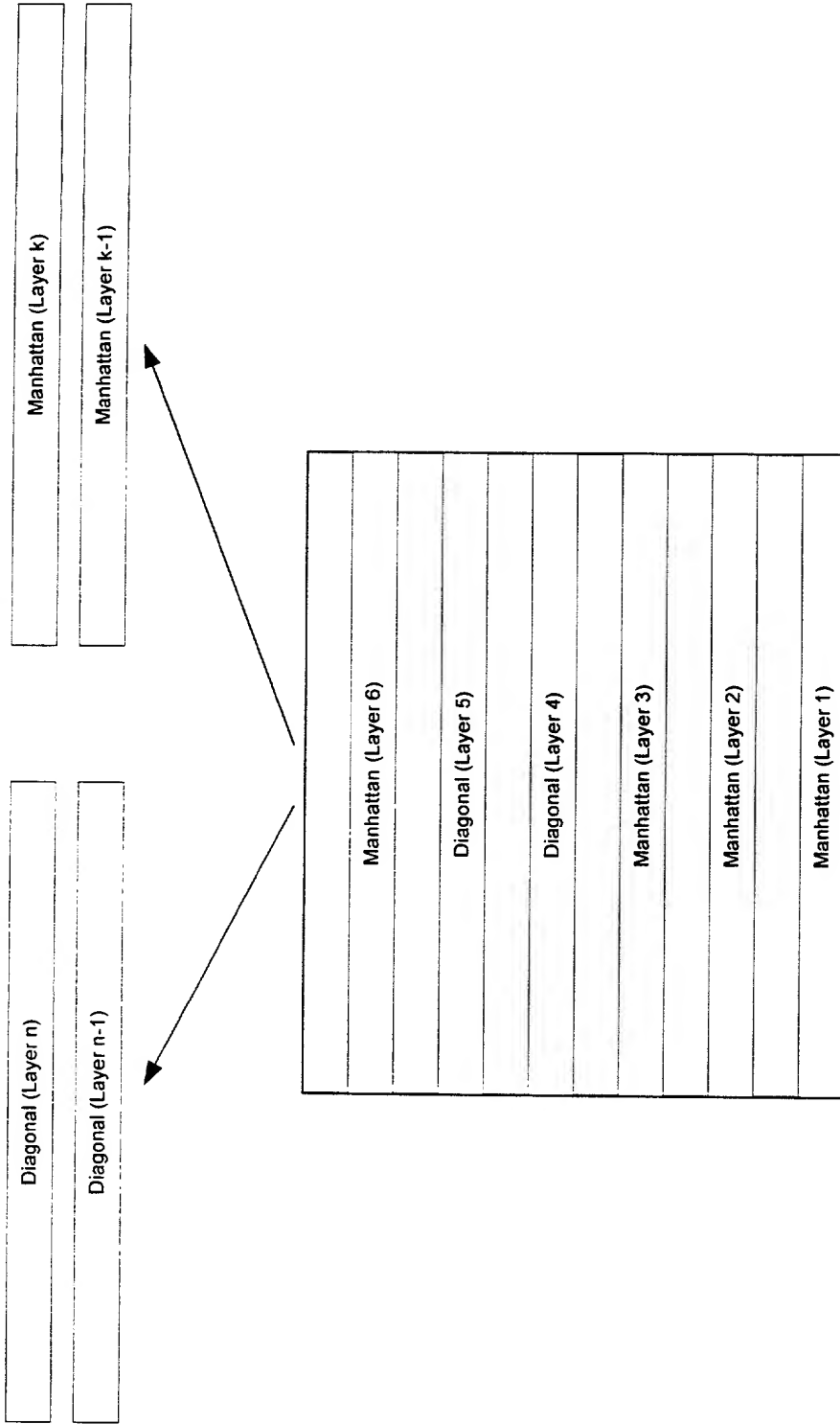
Hexalinear (Layer k)

Hexalinear (Layer k-1)

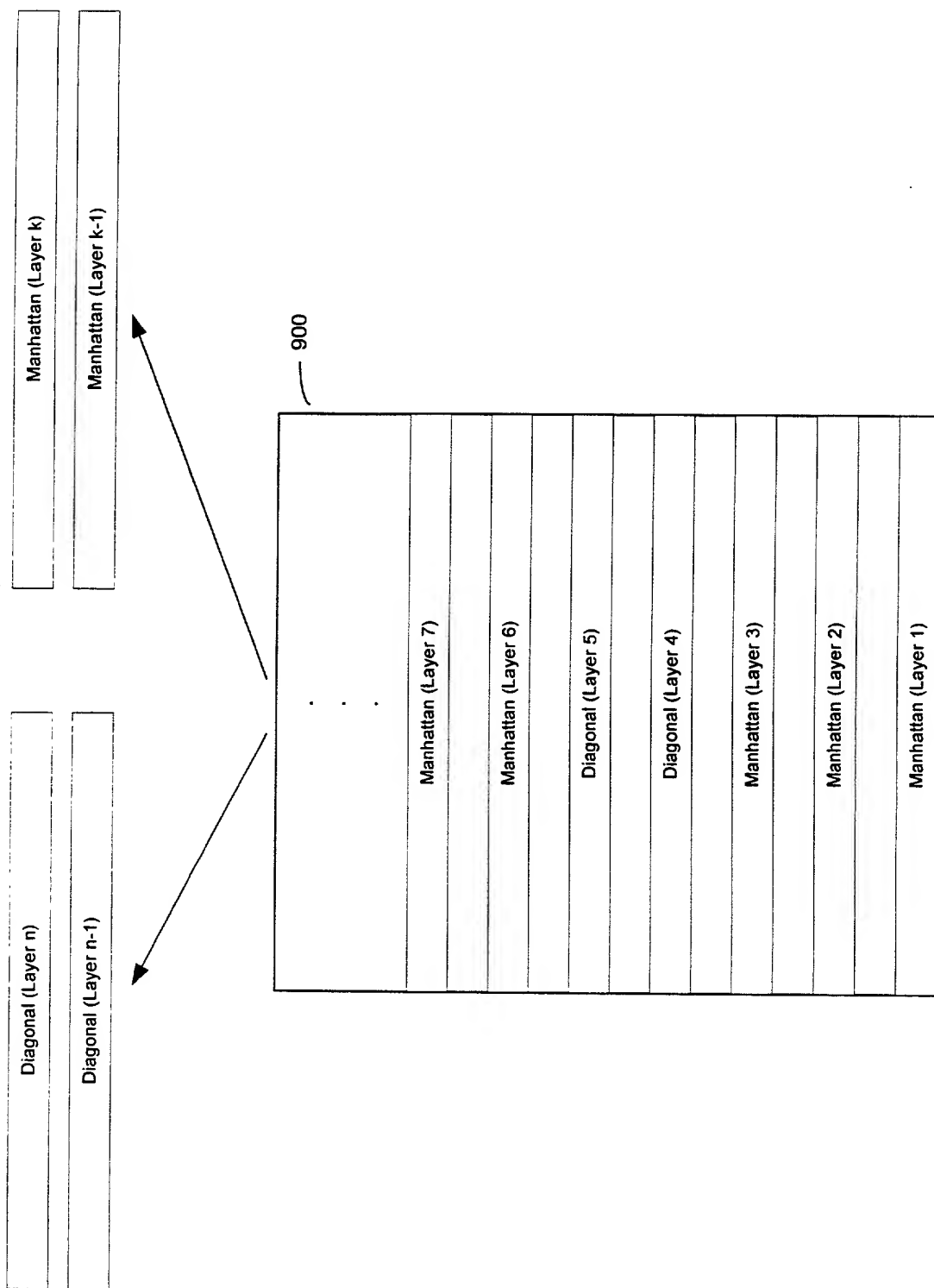
Figure 7



Figure 8: A diagram illustrating the structure of a multi-layered system. The diagram shows a central vertical stack of layers, with arrows pointing to specific layers on the left and right sides.



**Figure 8**



**Figure 9**

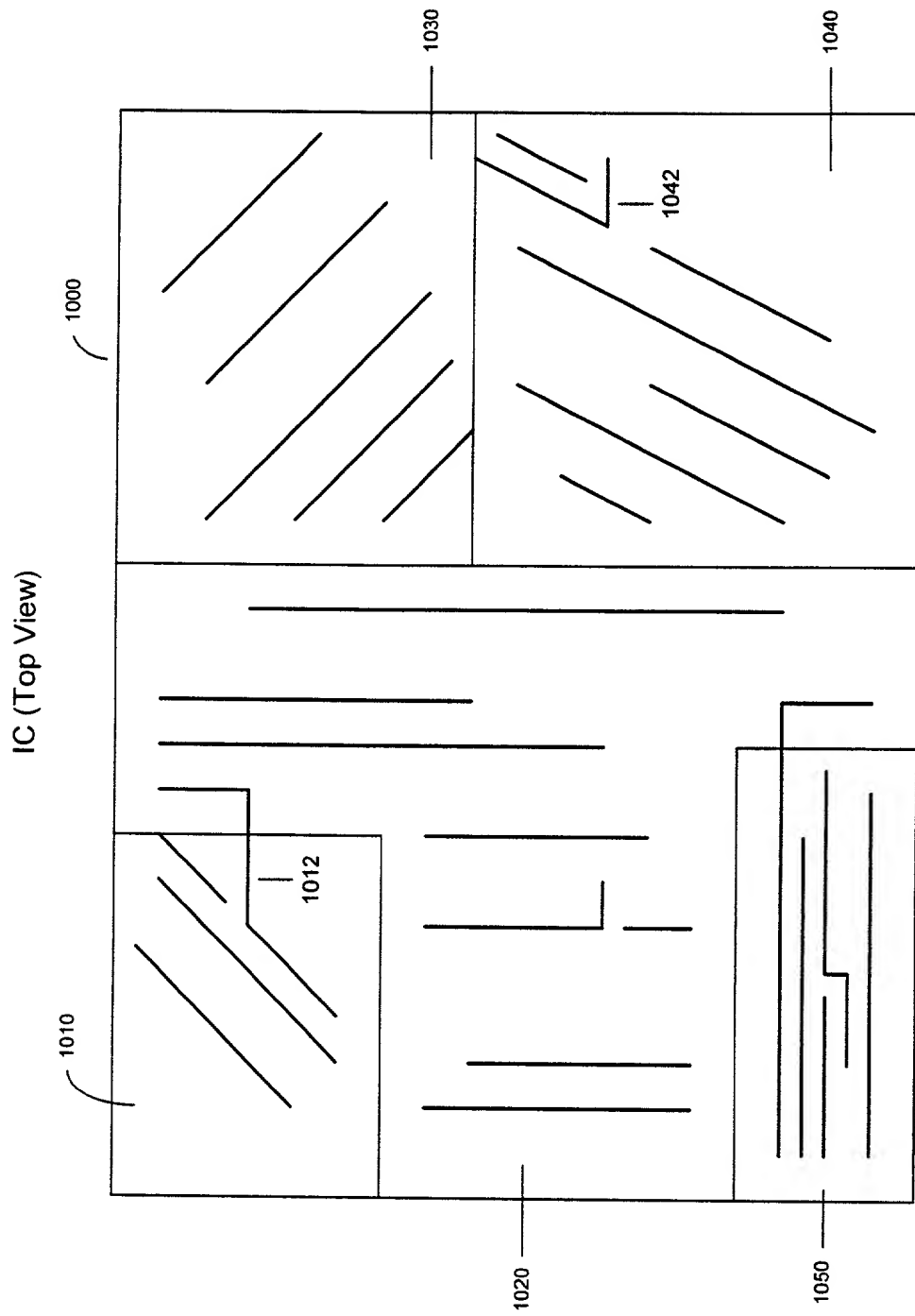


Figure 10

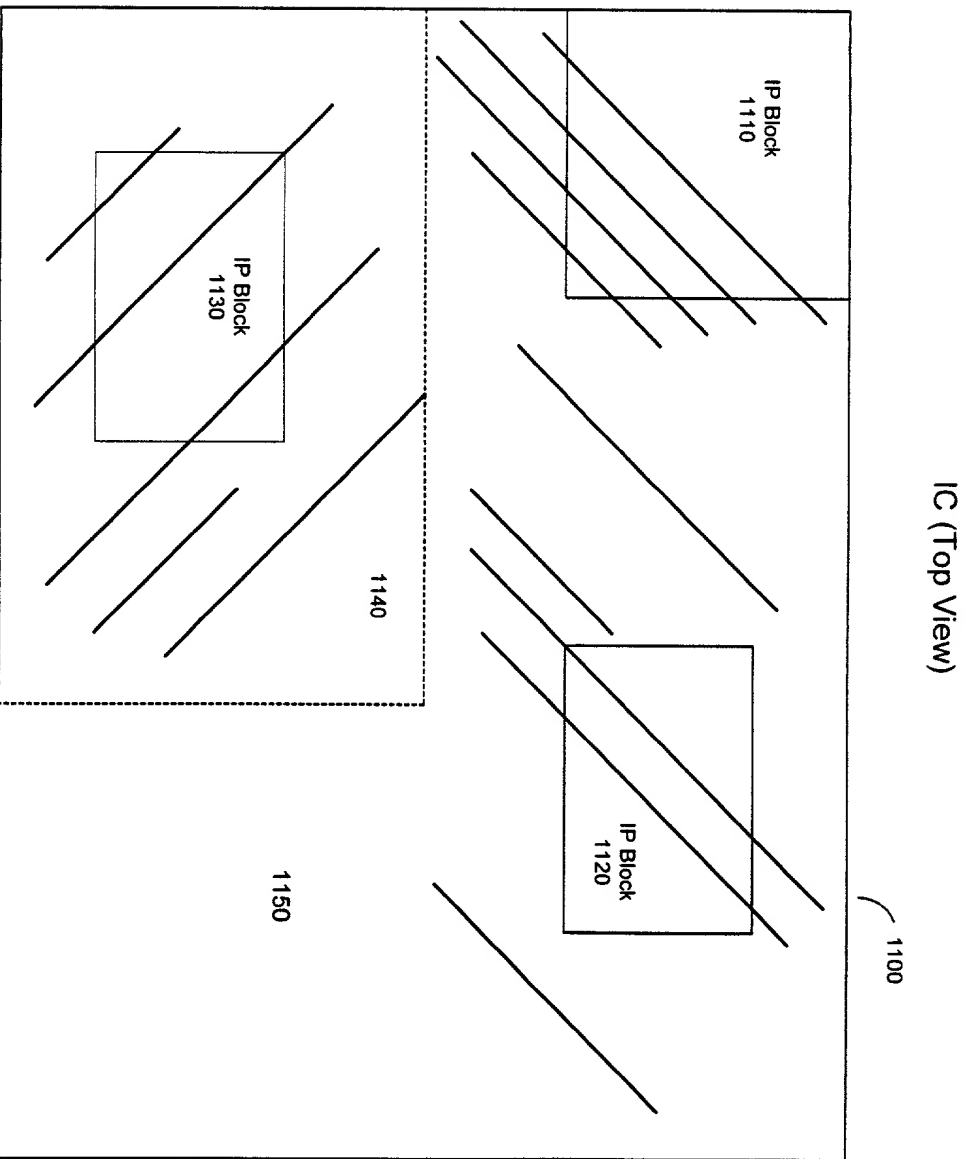
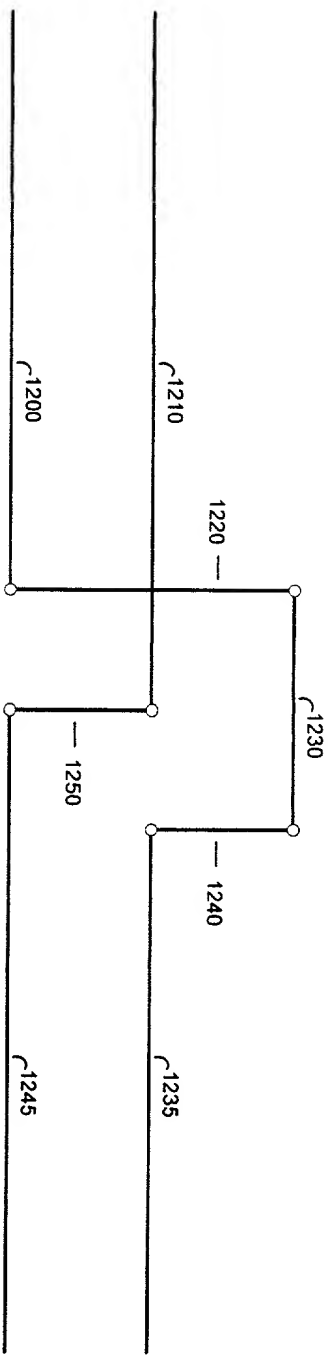
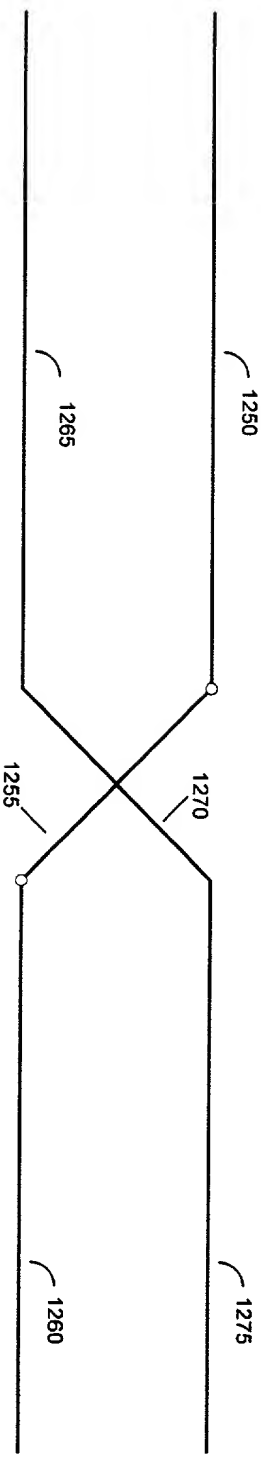


Figure 11



**Figure 12a**

**Prior Art**



**Figure 12b**

**Figure 13**

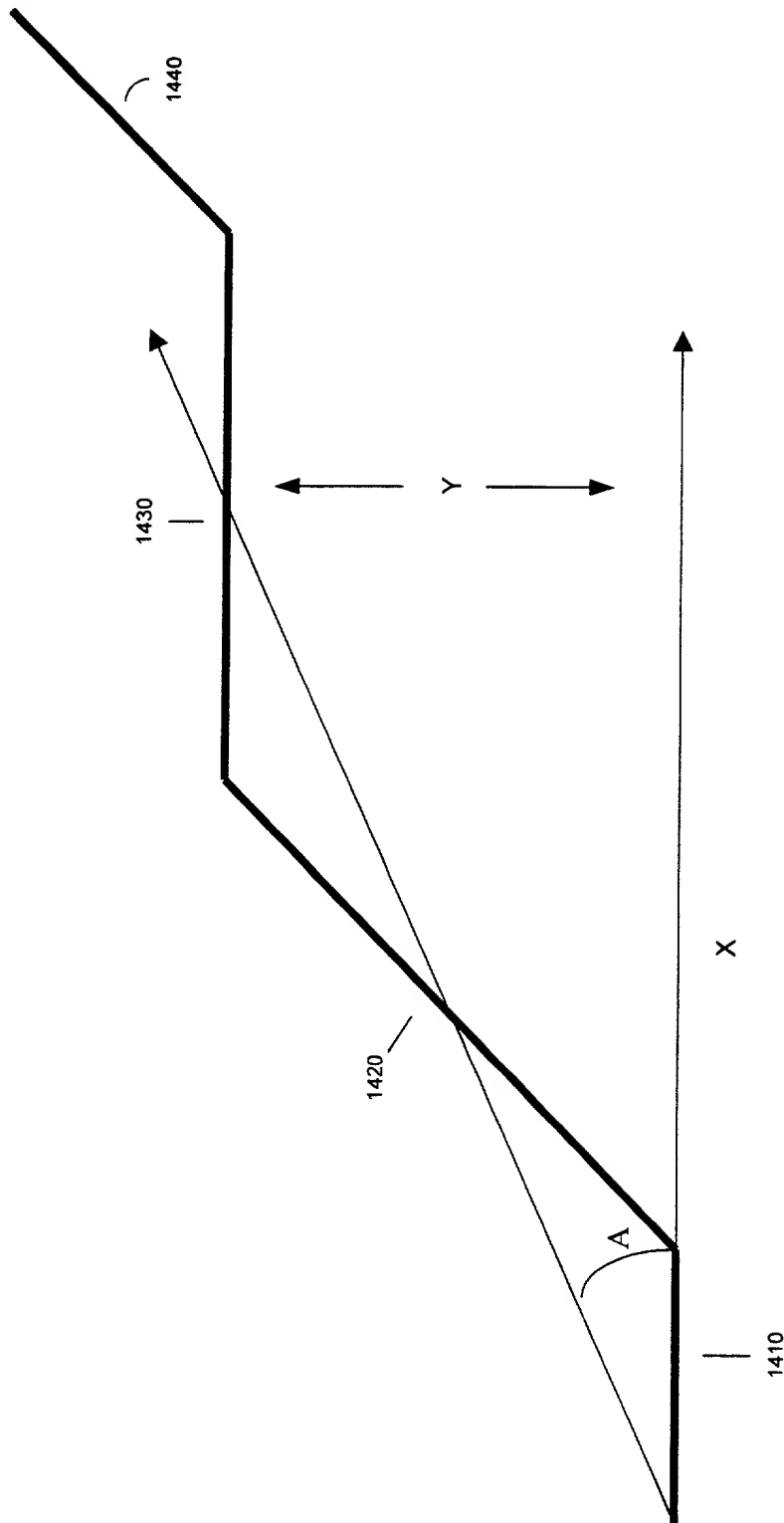


Figure 14

IC (Top View)

1500

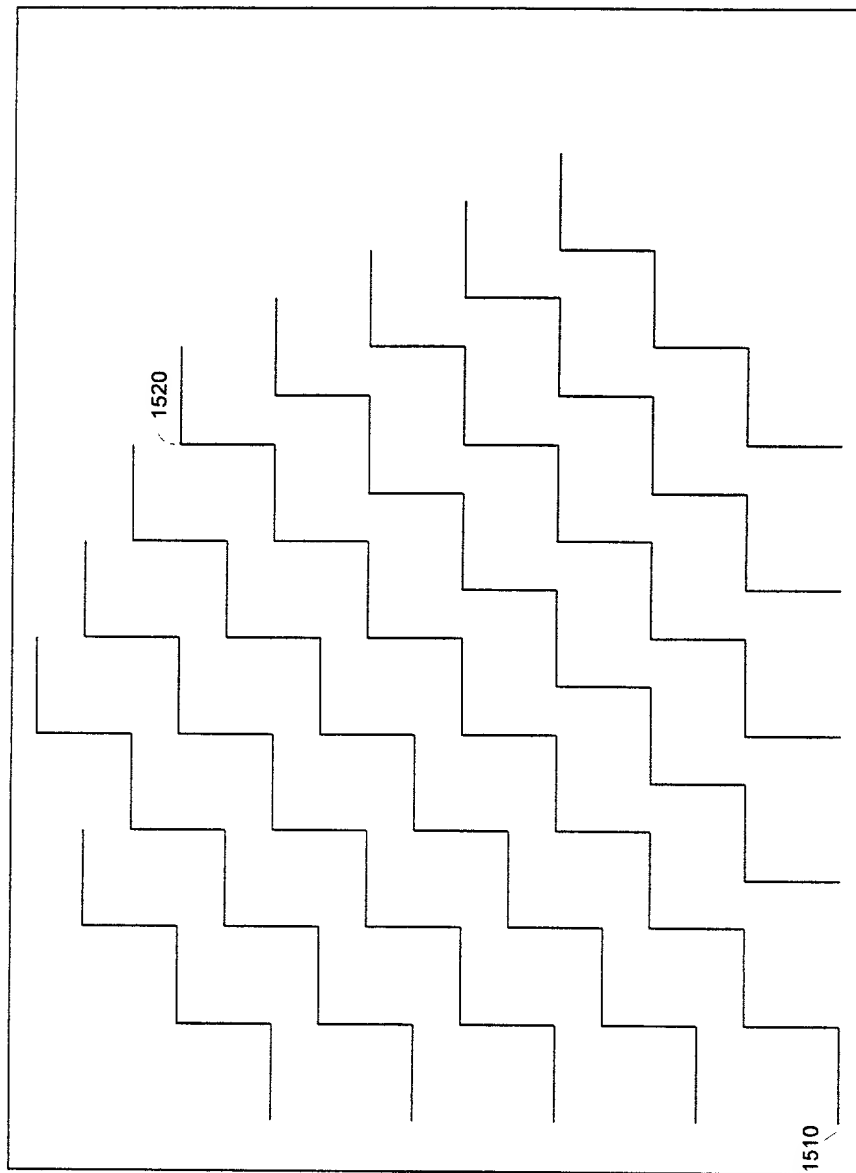


Figure 15



FIG. 16 is a top view of an integrated circuit (IC) 1600. The IC 1600 includes a clock input 1610, a first output 1620, a second output 1630, and a third output 1640. The clock input 1610 is connected to the first output 1620. The first output 1620 is connected to the second output 1630. The second output 1630 is connected to the third output 1640.

IC (Top View)

1600

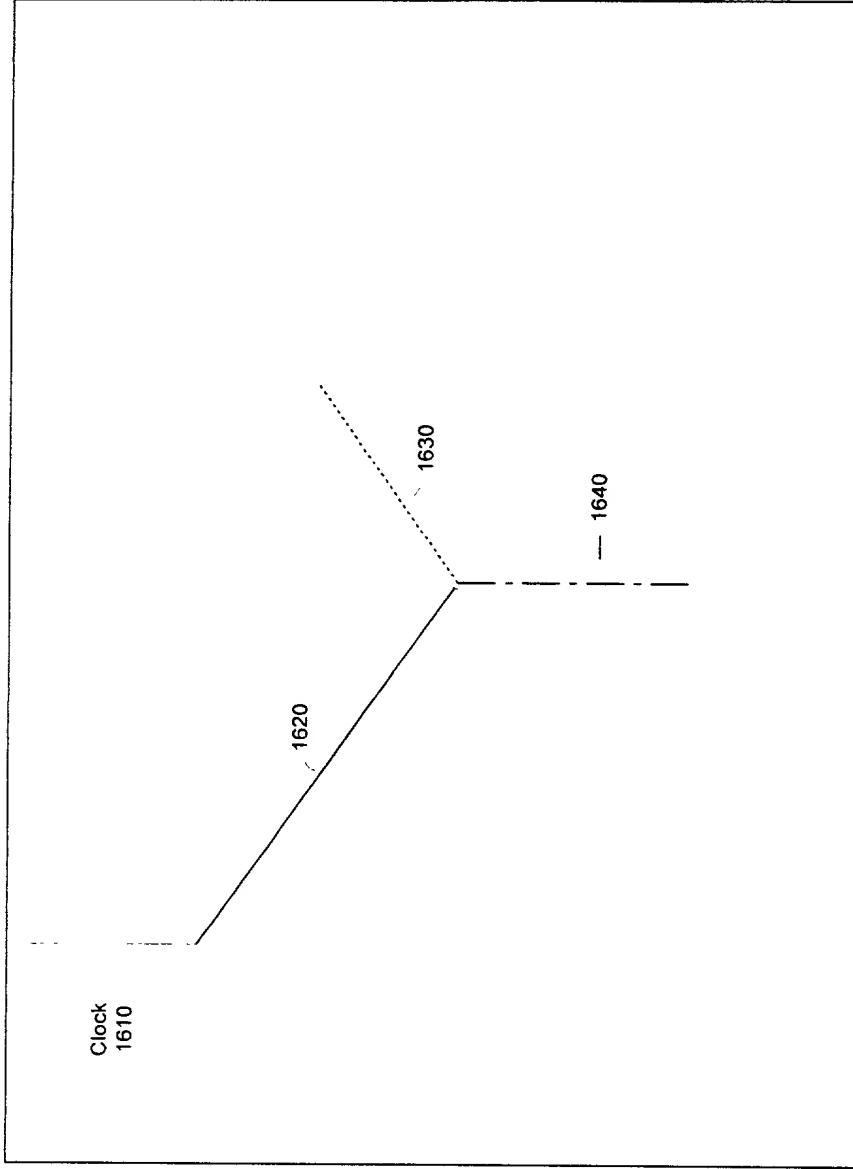


Figure 16